

Express Mail Number: EV339770263US  
Date of Mailing: July 18, 2003

Our Case No. 10519-109

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of:	)	
Scheuerlein et al.	)	
	)	Group Art Unit: Not Yet Assigned
Serial No.:	Not Yet Assigned	)
	)	Examiner: Not Yet Assigned
Filed:	Herewith	)
	)	
For:	Memory Device and Method for	)
	Selectable Sub-Array Activation	)

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to the obligation under 37 C.F.R. § 1.56 and in conformance with 37 C.F.R. §§ 1.97-1.99, Applicants hereby submit documents A1-A28 listed on the attached form PTO-1449 for consideration by the Examiner. In accordance with 37 C.F.R. § 1.98(d), copies of these documents are not enclosed since they were previously cited by or submitted to the Office in prior application serial number 09/943,655, which is being relied upon for an earlier filing date under 35 U.S.C. § 120.

The filing of this Information Disclosure Statement does not constitute an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b). Further, Applicants reserve the right to contest that any of the information submitted herewith is prior art against the present application.

Applicants also wish to inform the Examiner that the SD Memory Card Specification, Part 1, Physical Layer Specification, describes a part identification that is stored in a non-volatile memory device by the manufacturer of the memory device and can be interpreted by an application to indicate the maximum power level that can be dissipated by the memory device.

Dated: July 18, 2003

Respectfully submitted,



Joseph F. Hetz  
Reg. No. 41,070  
Attorney for Applicants

BRINKS HOFER  
GILSON & LIONE  
P.O. Box 10395  
Chicago, Illinois 60610  
(312) 321-4719

FORM PTO-1449	SERIAL NO. To be Assigned	CASE NO. 10519/109
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (use several sheets if necessary)	FILING DATE Herewith	GROUP ART UNIT To be Assigned
APPLICANT(S): SCHEUERLEIN ET AL.		

**REFERENCE DESIGNATION**      **U.S. PATENT DOCUMENTS**

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS/ SUBCLASS	FILING DATE
A1	5,925,996	07/20/99	MURRAY		
A2	6,055,180	04/25/00	GODESEN ET AL.		
A3	5,961,215	10/05/99	LEE ET AL.		
A4	6,034,882	03/07/00	JOHNSON ET AL.		
A5	5,835,396	11/10/98	ZHANG		
A6	6,236,587	05/22/01	GODESEN ET AL.		
A7	4,646,266	02/24/87	OVSHINSKY ET AL.		
A8	5,784,328	07/21/98	IRRINKI ET AL.		
A9	5,278,796	02/11/94	TILLINGHAST ET AL.		
A10	3,851,316	11/26/74	KODAMA		
A11	4,646,269	02/24/87	WONG ET AL.		
A12	4,873,669	10/10/89	FURUTANI ET AL.		
A13	5,383,157	01/17/95	PHELAN		
A14	4,744,061	05/10/88	TAKEMAE ET AL.		
A15	6,034,918	03/07/00	FARMWALD ET AL.		
A16	6,185,712	02/06/01	KIRIHATA ET AL.		
A17	6,070,222	05/30/00	FARMWALD ET AL.		
A18	6,212,121	04/03/01	RYU ET AL.		
A19	4,698,788	10/06/87	FLANNAGAN ET AL.		
A20	5,276,649	01/04/94	HOSHITA ET AL.		
A21	5,149,199	09/1992	KINOSHITA ET AL.		
A22	5,890,100	03/1999	CRAYFORD		
A23	6,385,074 B1	05/2002	JOHNSON ET AL.		
A24	5,977,746	11/1999	HERSHBERGER ET AL.		
A25	6,373,768 B2	04/16/2002	WOO ET AL.		
A26	5,107,139	04/21/1992	HOUSTON ET AL.		

**FOREIGN PATENT DOCUMENTS**

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES NO

EXAMINER INITIAL	OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)
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EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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(use several sheets if necessary)	APPLICANT(S): Scheuerlein ET AL.	

EXAMINER INITIAL	OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)	
	A27	"A 14ns 1Mb CMOS SRAM with Variable Bit-Organization," Wada et al., 1988 IEEE International Solid-State Circuits Conference, pages 252-253 (February 19, 1988).
	A28	"Partial Selection of Passive Element Memory Cell Sub-Arrays for Write Operation," U.S. Patent Application Serial No. 09/748,649, filed December 22, 2000; inventors: Roy E. Scheuerlein and Matthew P. Crowley.

EXAMINER	DATE CONSIDERED
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